



July 2013

# FCPF260N60E\_F152

## N-Channel SuperFET® II MOSFET

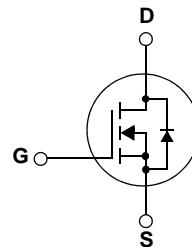
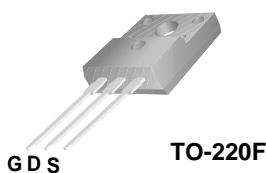
600 V, 15 A, 260 mΩ

### Features

- 650 V @  $T_J = 150^\circ\text{C}$
- Max.  $R_{DS(on)} = 260 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ.  $Q_g = 48 \text{ nC}$ )
- Low Effective Output Capacitance (Typ.  $C_{oss,\text{eff}} = 129 \text{ pF}$ )
- 100% Avalanche Tested

### Applications

- LCD / LED / PDP TV Lighting
- Solar Inverter
- AC-DC Power Supply



### Description

SuperFET® II MOSFET is Fairchild Semiconductor®'s first generation of high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This advanced technology is tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. Consequently, SuperFET® II MOSFET is suitable for various AC/DC power conversion for system miniaturization and higher efficiency.

### MOSFET Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter		FCPF260N60E_F152	Unit
$V_{DSS}$	Drain to Source Voltage		600	V
$V_{GSS}$	Gate to Source Voltage	- DC	$\pm 20$	V
		- AC ( $f > 1\text{Hz}$ )	$\pm 30$	V
$I_D$	Drain Current	- Continuous ( $T_C = 25^\circ\text{C}$ )	15*	A
		- Continuous ( $T_C = 100^\circ\text{C}$ )	9.5*	
$I_{DM}$	Drain Current	- Pulsed	(Note 1)	A
$E_{AS}$	Single Pulsed Avalanche Energy		(Note 2)	mJ
$I_{AR}$	Avalanche Current		(Note 1)	A
$E_{AR}$	Repetitive Avalanche Energy		(Note 1)	mJ
$dv/dt$	Peak Diode Recovery dv/dt		(Note 3)	20
	MOSFET dv/dt			100 V/ns
$P_D$	Power Dissipation	( $T_C = 25^\circ\text{C}$ )	36	W
		- Derate above $25^\circ\text{C}$	0.29	W/ $^\circ\text{C}$
$T_J, T_{STG}$	Operating and Storage Temperature Range		-55 to +150	$^\circ\text{C}$
$T_L$	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds		300	$^\circ\text{C}$

\*Drain current limited by maximum junction temperature

### Thermal Characteristics

Symbol	Parameter	FCPF260N60E_F152	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case	3.5	$^\circ\text{C/W}$
$R_{\theta CS}$	Thermal Resistance, Case to Heat Sink (Typical)	0.5	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	62.5	

## Package Marking and Ordering Information

Device Marking	Device	Package	Eco Status	Packaging Type	Quantity
FCPF260N60E	FCPF260N60E_F152	TO-220F	Green 	Tube	50

For Fairchild's definition of "green" Eco Status, please visit: [http://www.fairchildsemi.com/company/green/rohs\\_green.html](http://www.fairchildsemi.com/company/green/rohs_green.html).

## Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
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### Off Characteristics

$\text{BV}_{\text{DSS}}$	Drain to Source Breakdown Voltage	$V_{\text{GS}} = 0\text{V}, I_{\text{D}} = 10\text{mA}, T_J = 25^\circ\text{C}$	600	-	-	V
		$V_{\text{GS}} = 0\text{V}, I_{\text{D}} = 10\text{mA}, T_J = 150^\circ\text{C}$	650	-	-	V
$\Delta \text{BV}_{\text{DSS}} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_{\text{D}} = 10\text{mA}$ , Referenced to $25^\circ\text{C}$	-	0.67	-	$\text{V}/^\circ\text{C}$
$\text{BV}_{\text{DS}}$	Drain-Source Avalanche Breakdown Voltage	$V_{\text{GS}} = 0\text{V}, I_{\text{D}} = 15\text{A}$	-	700	-	V
		$V_{\text{DS}} = 480\text{V}, V_{\text{GS}} = 0\text{V}$	-	-	10	$\mu\text{A}$
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}} = 480\text{V}, T_C = 125^\circ\text{C}$	-	-	10	$\mu\text{A}$
		$V_{\text{GS}} = \pm 20\text{V}, V_{\text{DS}} = 0\text{V}$	-	-	$\pm 100$	nA
$I_{\text{GSS}}$	Gate to Body Leakage Current					

### On Characteristics

$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{GS}} = V_{\text{DS}}, I_{\text{D}} = 250\mu\text{A}$	2.5	-	3.5	V
$R_{\text{DS}(\text{on})}$	Static Drain to Source On Resistance	$V_{\text{GS}} = 10\text{V}, I_{\text{D}} = 7.5\text{A}$	-	0.22	0.26	$\Omega$
$g_{\text{FS}}$	Forward Transconductance	$V_{\text{DS}} = 20\text{V}, I_{\text{D}} = 7.5\text{A}$	-	15.5	-	S

### Dynamic Characteristics

$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}} = 25\text{V}, V_{\text{GS}} = 0\text{V}$ $f = 1\text{MHz}$	-	1880	2500	pF
$C_{\text{oss}}$	Output Capacitance		-	1330	1770	pF
$C_{\text{rss}}$	Reverse Transfer Capacitance		-	85	130	pF
$C_{\text{oss}}$	Output Capacitance	$V_{\text{DS}} = 380\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$	-	32	-	pF
$C_{\text{oss, eff.}}$	Effective Output Capacitance	$V_{\text{DS}} = 0\text{V to } 480\text{V}, V_{\text{GS}} = 0\text{V}$	-	129	-	pF
$Q_{\text{g}(\text{tot})}$	Total Gate Charge at 10V		-	48	62	nC
$Q_{\text{gs}}$	Gate to Source Gate Charge	$V_{\text{DS}} = 380\text{V}, I_{\text{D}} = 7.5\text{A}$ $V_{\text{GS}} = 10\text{V}$	-	7.4	-	nC
$Q_{\text{gd}}$	Gate to Drain "Miller" Charge		(Note 4)	-	17	nC
ESR	Equivalent Series Resistance	$f = 1\text{MHz}$	-	5.8	-	$\Omega$

### Switching Characteristics

$t_{\text{d}(\text{on})}$	Turn-On Delay Time	$V_{\text{DD}} = 380\text{V}, I_{\text{D}} = 7.5\text{A}$ $V_{\text{GS}} = 10\text{V}, R_G = 4.7\Omega$	-	20	50	ns
$t_r$	Turn-On Rise Time		-	11	32	ns
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time		-	89	188	ns
$t_f$	Turn-Off Fall Time		(Note 4)	-	13	36

### Drain-Source Diode Characteristics

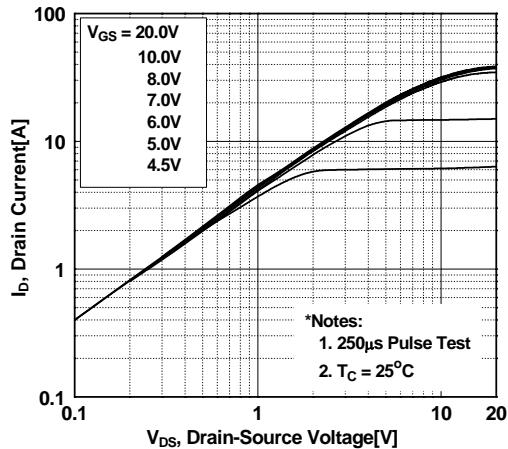
$I_S$	Maximum Continuous Drain to Source Diode Forward Current	-	-	15	A	
$I_{\text{SM}}$	Maximum Pulsed Drain to Source Diode Forward Current	-	-	45	A	
$V_{\text{SD}}$	Drain to Source Diode Forward Voltage	$V_{\text{GS}} = 0\text{V}, I_{\text{SD}} = 7.5\text{A}$	-	-	1.2	V
$t_{\text{rr}}$	Reverse Recovery Time	$V_{\text{GS}} = 0\text{V}, I_{\text{SD}} = 7.5\text{A}$ $dI_F/dt = 100\text{A}/\mu\text{s}$	-	270	-	ns
$Q_{\text{rr}}$	Reverse Recovery Charge		-	3.6	-	$\mu\text{C}$

#### Notes:

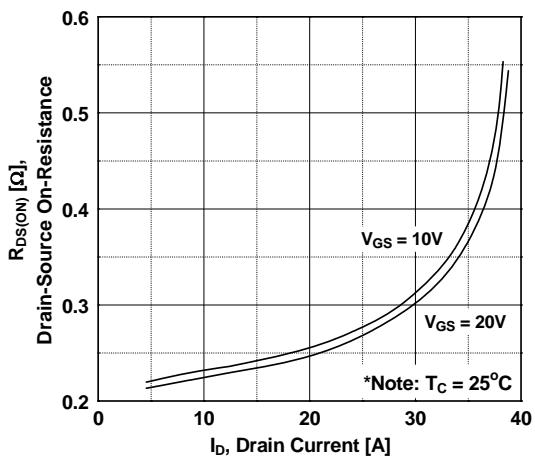
1. Repetitive Rating: Pulse width limited by maximum junction temperature
2.  $I_{AS} = 3\text{A}, V_{DD} = 50\text{V}, R_G = 25\Omega$ , Starting  $T_J = 25^\circ\text{C}$
3.  $I_{SD} \leq 7.5\text{A}, dI/dt \leq 200\text{A}/\mu\text{s}, V_{DD} \leq BV_{\text{DSS}}$ , Starting  $T_J = 25^\circ\text{C}$
4. Essentially Independent of Operating Temperature Typical Characteristics

## Typical Performance Characteristics

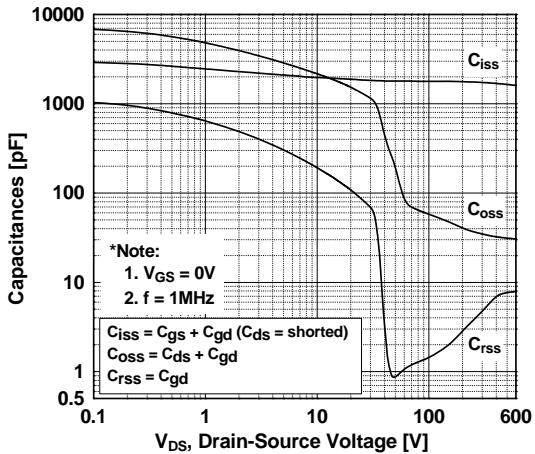
**Figure 1. On-Region Characteristics**



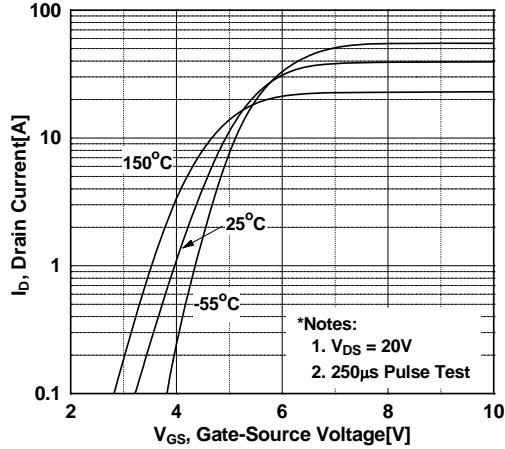
**Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage**



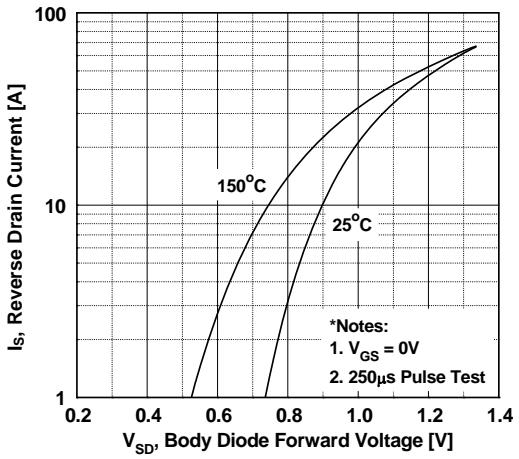
**Figure 5. Capacitance Characteristics**



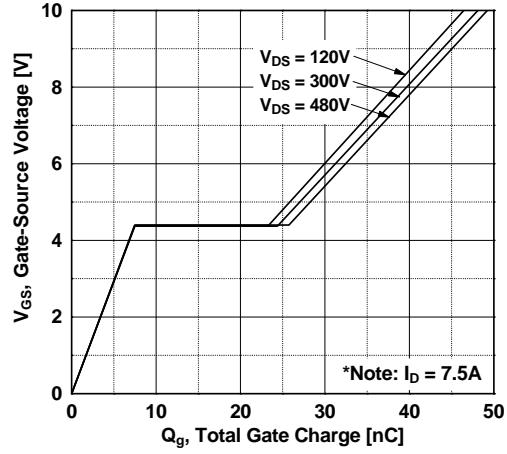
**Figure 2. Transfer Characteristics**



**Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature**

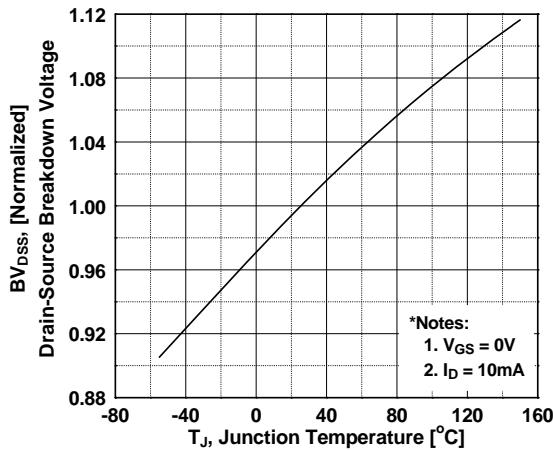


**Figure 6. Gate Charge Characteristics**

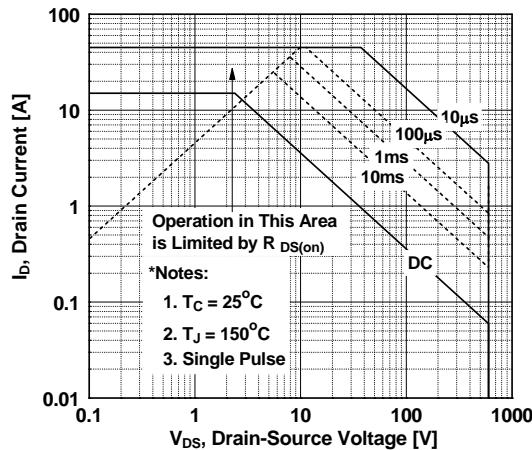


## Typical Performance Characteristics (Continued)

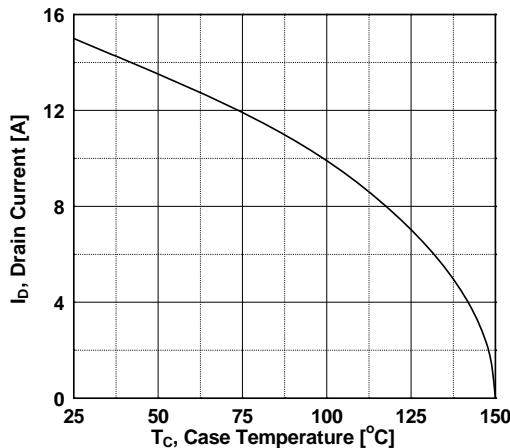
**Figure 7. Breakdown Voltage Variation vs. Temperature**



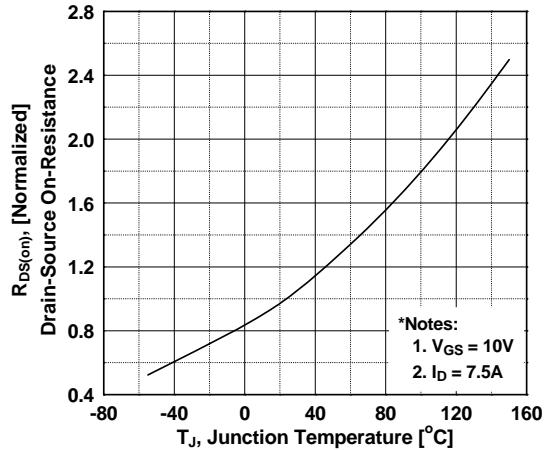
**Figure 9. Maximum Safe Operating Area vs. Case Temperature**



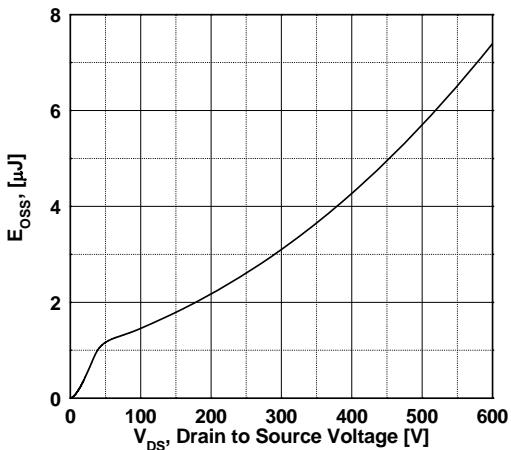
**Figure 11. Maximum Drain Current**



**Figure 8. On-Resistance Variation vs. Temperature**

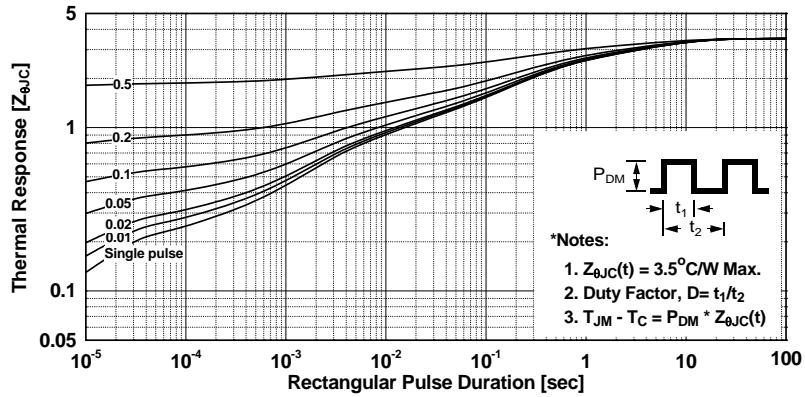


**Figure 10. Eoss vs. Drain to Source Voltage Switching Capability**

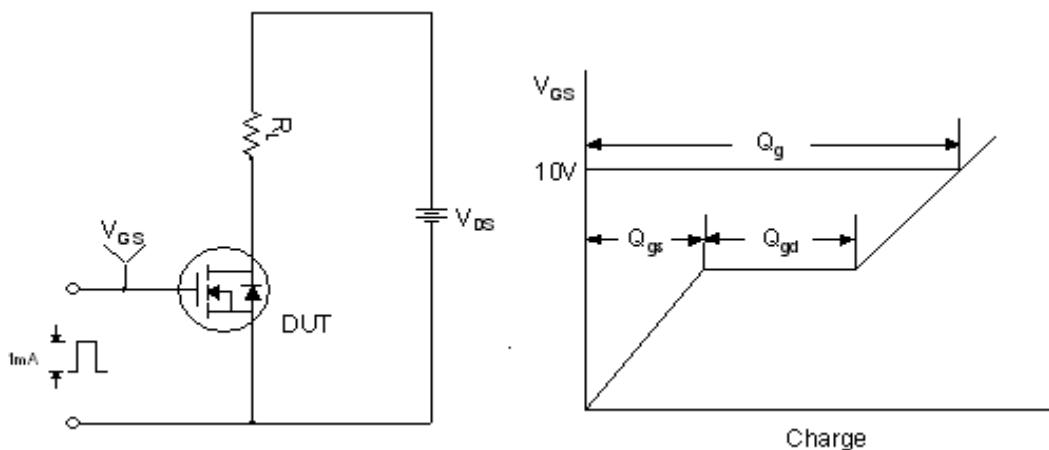


## Typical Performance Characteristics (Continued)

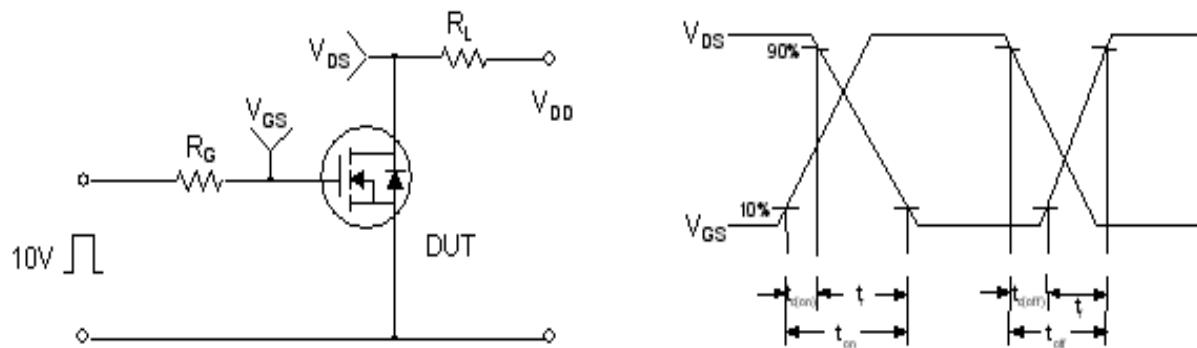
Figure 12. Transient Thermal Response Curve



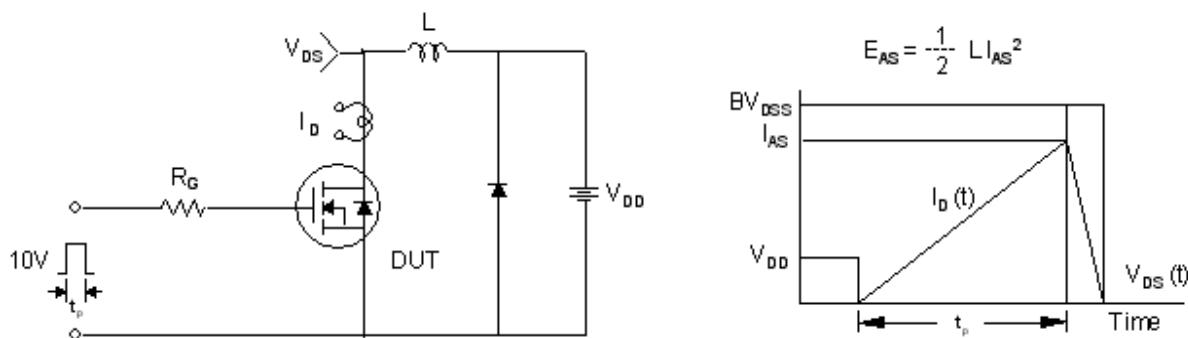
Gate Charge Test Circuit & Waveform



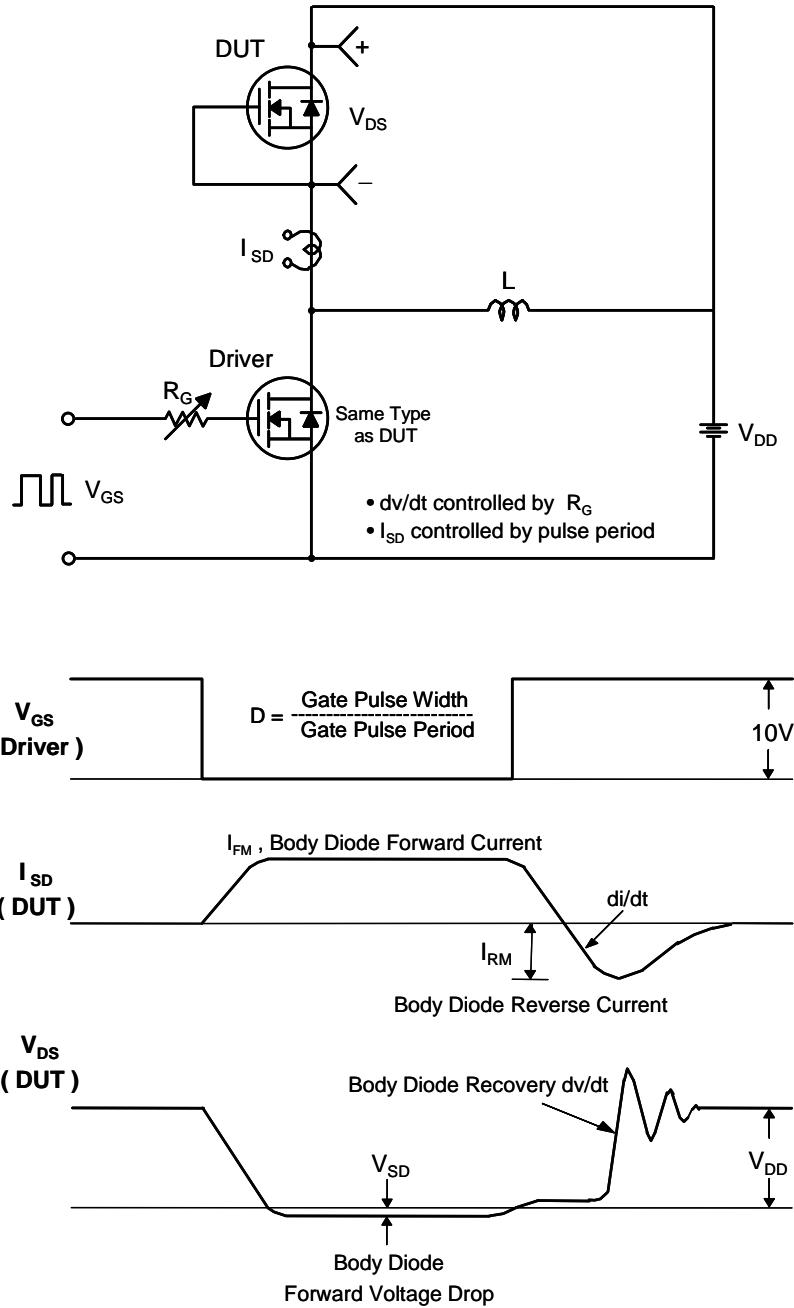
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms

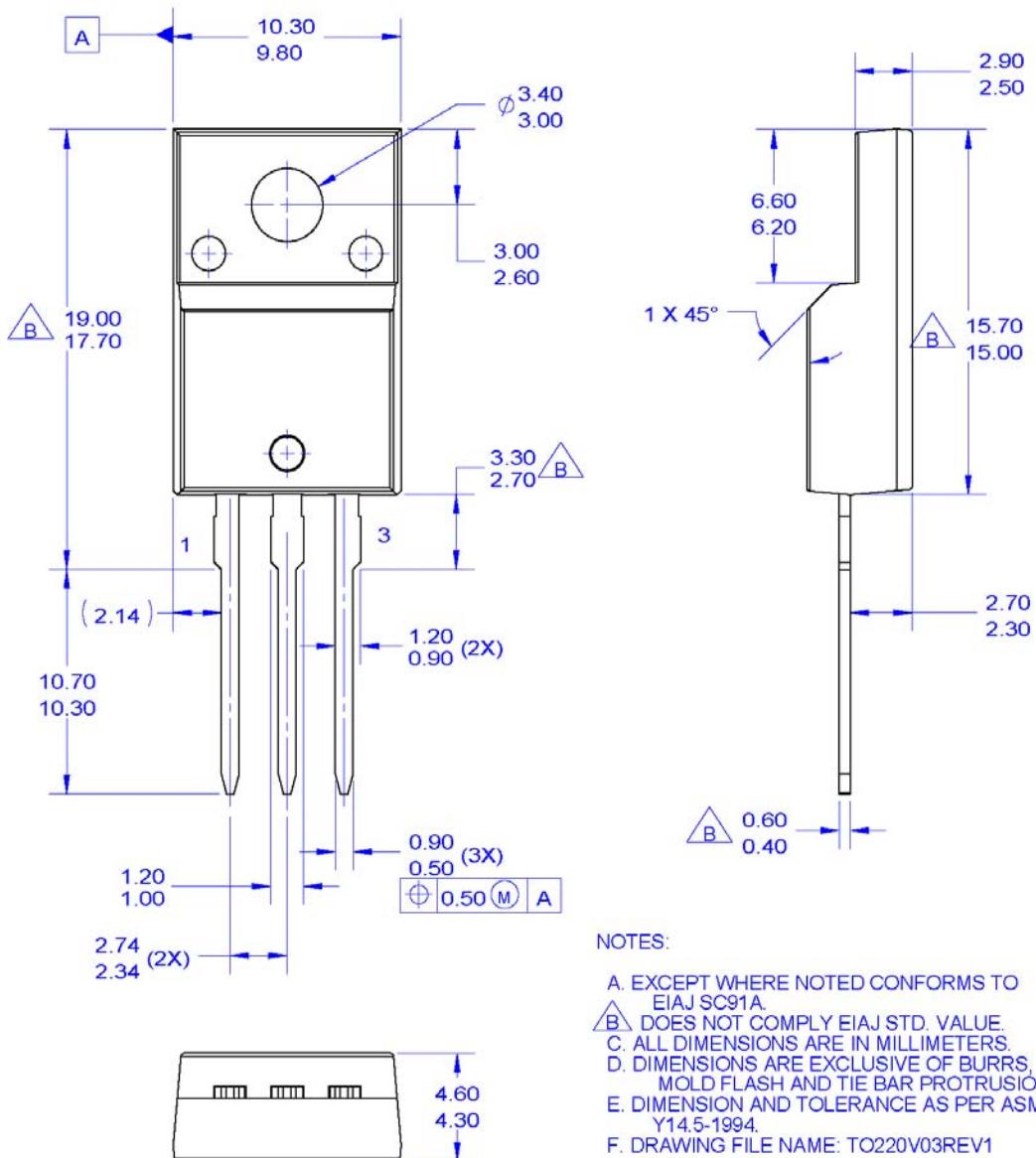


## Peak Diode Recovery dv/dt Test Circuit &amp; Waveforms



### **Mechanical Dimensions**

TO-220F



\* Front/Back Side Isolation Voltage : AC 2500V

## **TO-220, MOLDED, 3LD, FULL PACK, EIAJ SC91**

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Rev. I64

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"LifeElectronics" LLC

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- Техническую поддержку проекта.
- Защиту от снятия компонента с производства.
- Оценку стоимости проекта по компонентам.
- Изготовление тестовой платы монтаж и пусконаладочные работы.



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